

Sanjay Siva Kumar Surampudi

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Career Objective

Aspiring VLSI engineer with a strong foundation in digital design and hardware concepts, complemented by basic knowledge of Data Structures and Algorithms and C programming. Seeking an opportunity to apply academic learning and problem-solving skills in designing and optimizing scalable semiconductor solutions. Eager to contribute to innovative projects while continuously enhancing technical expertise in VLSI design and verification

Skills

- Digital electronics and logic design
- verilog HDL
- C Programming and DSA
- Problem Solving in Digital Design and DSA

Certifications

- cadence semiconductor 101
- cadence Digital IC Design
- cadence Verilog
- C Essentials — Cisco Networking Academy
- Problem Solving (Basic) — HackerRank
- Microsoft Certified: Power Platform Fundamentals — Microsoft excel

Projects

Mini Projects

- **BCD-7segment display** — Using proteus software , created the gate level netlist.
- **TIC TAC TOE Game** — Developed using C++ language.
- **Bluetooth Car** — Smartphone-controlled car using Arduino UNO.
- **Home automation** — Home gadgets are controlled using smartphone.

Education

B.Tech in Electronics and Communication Engineering,	2024 – Present
<ul style="list-style-type: none">• Aditya University, Surampalem• CGPA: 8.73%	
Board of Intermediate Education, AP	2022 – 2024
<ul style="list-style-type: none">• Sri Chaitanya Junior College, Kakinada• Percentage: 93.8%	
Board of Secondary Education, AP	2022
<ul style="list-style-type: none">• Bhashyam [E.M] School, Ramachandrapuram• Percentage: 90.5%	